



2036

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicant: Contopanagos

Serial No: 10/074,294

Filing Date: 2/12/02

Title: On-Chip Inductor Having Improved Quality Factor and Method of Manufacture Thereof

Examiner: Greene, Pershelle

Art Group: 2826

Docket No: BP 2108

Date: 9/27/03

Honorable Commissioner of
Patents and Trademarks,
Washington, D.C. 20231

In response to an Office Action mailed on 6/27/03 regarding the above-captioned patent application, the applicant respectfully submits the following:

In response to the restriction requirement, the applicant elects claims 1 - 15 and therefore respectfully requests that claims 16- 30 be withdrawn without prejudice.

RESPECTFULLY SUBMITTED,

By:

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TECH

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CERTIFICATE OF MAILING
37 C.F.R. 1.8

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, DC 20231, on the date below:

9/29/03

Date

Alison Shanon

Signature